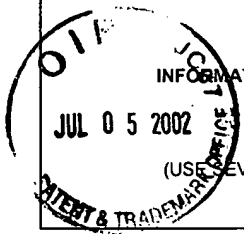


FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. IMEC211.001AUS	APPLICATION NO. 09/591,026
INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (USE SEVERAL SHEETS IF NECESSARY)		APPLICANT Vandersteen et al.	
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U.S. PATENT DOCUMENTS							
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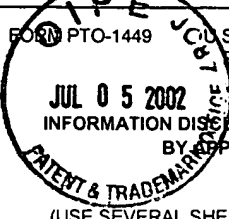
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EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
<i>AS</i>	Yah Wah Michael Chia et al., "Novel adaptive linearization scheme for multicarrier power amplifier using samples of multiple channels (SMC)", Vehicular Technology Conference, IEEE, May 1999, pages 1084-1088.
<i>SC</i>	Schneider, K.W. et al., "Efficient simulation of multi-carrier digital communication systems in nonlinear channel environments", Military Communications Conference, IEEE, October 1992, pages 339-344.
<i>PH</i>	Pino, J.L. et al., "Cosimulating synchronous DSP applications with analog RF circuits", Asilomar Conference on Signals, Systems and Computers, IEEE, November 1998, pages 1710-1714.
<i>GV</i>	Vandersteen, G. et al., "A methodology for efficient high-level data flow simulation of mixed-signal front-ends of digital telecom transceivers", Design Automation Conference, June 2000, pages 440-445.
<i>PS</i>	Schaumont, P. et al. "Synthesis of multirate and variable rate circuits for high speed telecommunications applications", European Design and Test Conference, pages 542-546.

EXAMINER <i>AS</i>	DATE CONSIDERED <i>7/4/04</i>
*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.	

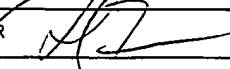
 <p>FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(USE SEVERAL SHEETS IF NECESSARY)</p>	ATTY. DOCKET NO. IMEC211.001AUS	APPLICATION NO. 09/591,026
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EXAMINER INITIAL 	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
	How-Siang Yap, "Designing to digital wireless specifications using Circuit Envelope simulation", Asia Pacific Microwave Conference, December 1997, pages 173-176.

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